


Name	VENKATACHALAM . K			
Department	ECE			
Qualification	B.E., M.E.,			
Area of Specialization	EMBEDDED SYSTEMS			
Date of Joining (KSR IET)	02-06-2014			
Experience (as on 01.06.2020)	Teaching: 6Y 1M	Industry: -	Others: -	
Number of Papers Published	National Journals : - International Journals : 01			
Number of Papers Presented	National Conferences : 05 International Conferences : 02			
Books Published	National Publisher : - International publisher : -			
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Publications (International Journals)-1

- 1. International Journal of Engineering & Technology “ Performance Analysis of VLSI Floor planning Based on Artificial Bee Colony Algorithm”**

Publications (National Journals)

NIL

Publications (International Conferences)

- 1. Attended International Conference in Muthayammal Engineering College presented on Implementation of Smart Chair with BLE.**
- 2. Attended International Conference in EASA College of Engineering and Technology and presented on Implementation of Enhanced Carry Select Adder with an Efficient Design Metrics.**

Publications (National Conferences)

1. Attended National Conference in Park College of Engineering and Technology and presented on Dynamic Power Management Technique for multi-core based Embedded Mobile Devices.
2. Attended National Conference in United Institute of Technology and presented paper on Implementation of Enhanced Carry Select Adder with an Efficient Design Metrics.
3. Attended National Conference in Institute of Road and Transport Technology and presented paper on An Area efficient Enhanced Carry Select Adder for Low power Application
4. Attended National Conference in PSNA College of Engineering and Technology and presented paper on Shock Preventer using PIC Microcontroller.
5. Attended National Conference in K S R Institute for Engineering and Technology and presented paper on Implementation of Enhanced Carry Select Adder with an Efficient Design Metrics.

PROGRAMMES ATTENDED (Seminar/ Workshop/ FDP / STTP others)

S. No	Name of the Programme	Place	Date
1.	One Day Seminar on “Cyber Security & Ethical Hacking”	K.S.R. Institute for Engineering and Technology	05.01.2019
2.	One Day National Level Workshop on “Antenna Design and Simulation using ANSYS HFSS”	K.S.Rangasamy College of Technology	6.03.2017
3.	Faculty Development Program on “Engineering challenges for smart & Digital India: IOT Applications”	K.S.Rangasamy College of Technology	20.11.2017 & 1.12.2017
4.	National Level Workshop on “VLSI DESIGN (using TANNER EDA & MENTOR GRAPHICS TOOLS)”	K.S.R. Institute for Engineering and Technology	30.08.2013 & 31.08.2013
5.	National Level Workshop on “Mentoring For Innovative Engineering”	Mahendra Engineering College	13.12.2010 & 15.12.2010
6.	Faculty Development Program on Signals and systems	Vivekanandha college of engineering for women	3.6.2019 & 9.6.2019
7.	One Day Seminar on ”how to write proposal ”	IIT MADRAS	12.02.2020

8.	Faculty Development Program on Digital Electronics	Saveethe engineering college	25.05.2020 & 29.05.2020
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PROGRAMMES ORGANIZED (Seminar/Conference/Workshop/FDP/STTP/Other)

S. No.	Name of the Programme	Type of Programme	Date
1.	Three days Workshop on” Project Based Learning in Embedded System Design using ARDUINO platform”	Workshop	09.08.17 & 11.08.17

AWARDS WON

S. No	Name of the Award	Academic Year
1.	Best Performance Award for producing 100% result in the Subject “Professional Ethics in Engineering”.	2017-2018